



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

A110-US

Applicant : John H. Coleman

Appl. No. : 09/800,213

Filed : March 6, 2001

For : SEMICONDUCTOR FABRICATION AND STRUCTURE FOR
FIELD-EFFECT AND BIPOLAR TRANSISTOR DEVICES

INFORMATION DISCLOSURE STATEMENT - MAILING DATE CORRECTED

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

April 5, 2001
Date of Deposit

Peter A. Businger
Attorney Name

27,862
PTO Reg. No.

Peter A. Businger
Signature

April 5, 2001
Date of Signature

Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicant respectfully requests that the 16 documents which are listed on the attached Form PTO-1449 be made of record in the above-identified patent application. A full copy is enclosed herewith of each of the documents, except only of the title page of U.S. Patent No. 6,144,072. In full the latter includes 135 pages; please advise if so required.

Respectfully submitted,

Peter A. Businger
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